



Koucheng Wu  
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San Jose, CA 95132  
Date: June 20, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Re: Inventor's Updated Address**

To Whom It May Concern:

I received the Office Action regarding to the patent application #10/781,112 (see below). The Office Action came to me late (because I am currently working overseas) so I request for extension of time.

Patent Application No.: **10/781,112**

Patent Title: "NOR-Type Channel-Program Channel-Erase Contactless Flash  
Memory on SOI"

Inventor: Koucheng Wu

Filing Date: February 18, 2004

I sent a letter on 02/14/2005 to USPTO for inventor's address change. I want to confirm that you have my updated address (see below) on file. Please note the below address is the same address when the patent application was filed in.

**3074 Vesuvius Lane  
San Jose, CA 95132  
U. S. A.**

Thanks for your support. Please let me know if there is any question.

Sincerely yours,

A handwritten signature in cursive script, appearing to read "Koucheng Wu".

Koucheng Wu